

TIME-TABLE

TIME: 10.00 AM TO 01.00 PM

M.TECH. I SEMESTER (RA18 REGULATION - 2018 & 2019 BATCHES) SUPPLEMENTARY EXAMINATIONS, JULY/AUGUST - 2023

Date & Day POWER ELECTRONICS 02.08.2023 WEDNESDAY Special Machines		EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING Advanced Data Structures
		Digital System Design	Digital System Design	Computer Integrated Manufacturing	
04.08.2023 Analysis of Power Electronic Converters		RTL Simulation and Synthesis with PLDs	RTL Simulation and Synthesis with PLDs	Advanced Computer Aided Design & Manufacturing	Machine Learning
08.08.2023 Research TUESDAY Methodology and IPR		Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR
10.08.2023 Power Electronic THURSDAY Control of DC Drives			Analog Integrated Circuit Design	Advanced Material Technology	Distributed Systems
14.08.2023 MONDAY		Embedded C	Embedded C	-	Software Process &Project Management
17.08.2023 THURSDAY HVDC Transmission		ARM Architectures	-	Special Manufacturing Process	

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CONTROLLER OF EXAMINATIONS



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TIME: 10.00 AM TO 01.00 PM

M.TECH. I SEMESTER (RA15 REGULATION - 2017 BATCH) SUPPLEMENTARY EXAMINATIONS, JULY/AUGUST - 2023

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING	SOFTWARE ENGINEERING
02.08.2023 WEDNESDAY	Machine Modelling & Analysis	Elective – II Digital System Design	Elective – I Digital System Design	Elective – II Precision Engineering	Big data and Analytics	Software Requirements and Estimation
04.08.2023 FRIDAY	Analysis of Power Electronic Converters	Elective – I VLSI Technology and Design	VLSI Technology and Design	Advanced Computer Aided Design & Manufacturing	Machine Learning	Web Technologies and Services
08.08.2023 TUESDAY	Modern Control Theory	Real Time Operating System	CMOS Analog Integrated Circuit Design	Theory of Metal Cutting	Elective – II Software Architecture and Design Patterns	Elective — I Information Security and Audit
10.08.2023 THURSDAY	Power Electronic Control of DC Drives	Embedded System Design	Embedded System Design	Elective – III Applied Tribology	Distributed Systems	Software Development Methodologies
14.08.2023 MONDAY	Elective – II Dynamics of Electrical Machines	Elective – III TCP/IP Networks	Elective – II CPLD and FPGA Architectures and Applications	Automation in Manufacturing	Elective – I Software Process and Project Management	Elective – II Cyber Security and Cyber Laws
17.08.2023 THURSDAY	Elective – I High Voltage DC Transmission	ARM Architectures	Elective – III Advanced Data Communications	Elective – I Special Manufacturing Process	Advanced Data Structures and Algorithms	Advanced Data Structures and Algorithms

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TIME-TABLE

TIME: 10.00 AM TO 01.00 PM

M. TECH. II SEMESTER (RA18 REGULATION - 2018 & 2019 BATCHES) SUPPLEMENTARY EXAMINATIONS, JULY/AUGUST - 2023

Date & Day POWER ELECTRONICS		EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE 8 ENGINEERING	
01.08.2023 TUESDAY	Power Electronic Control of AC Drives	Embedded OS & RTOS	CMOS VLSI Design	Manufacturing Systems Simulation	Advanced Algorithms	
03.08.2023 THURSDAY	Power Electronic Applications to Renewable Energy	-	ASIC and SOC	Theory of Metal Cutting and Forming	Big Data Analytics	
07.08.2023 MONDAY	Microprocessor and Microcontroller	Advanced Digital Signal Processing	Advanced Digital Signal Processing	Advanced Tool Design	Deep Learning	
09.08.2023 WEDNESDAY		Embedded Networking High Speed Digital Design	Embedded Networking High Speed Digital Design	NDE & NDT	-	
11.08.2023 FRIDAY Power Quality Dynamics of Electrical Machines		System On Chip Architecture	-	-	Cryptography and Network Security	

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M.TECH. II SEMESTER (RA15 REGULATION - 2017 BATCH) SUPPLEMENTARY EXAMINATIONS, JULY/AUGUST - 2023

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING	SOFTWARE ENGINEERING
01.08.2023 TUESDAY	Power Electronic Control of AC Drives	CPLD and FPGA Architectures and Applications	Real Time Operating Systems	Manufacturing Systems Simulation Modelling & Analysis	Advanced Network Programming	Software Process and Project Management
03.08.2023 THURSDAY	Neural And Fuzzy Systems	Hardware Software Co-Design Network Security and Cryptography	CMOS Mixed Signal Circuit Design	Quality Engineering in Manufacturing	High Performance Computing	Big Data and Analytics
		Digital Signal Embedded C			Adhoc and Sensor Networks	§4
07.08.2023 MONDAY	Microprococcor and	Materials Technology	Database Security	Scripting Languages		
09.08.2023 WEDNESDAY	Flexible AC Transmission Systems (FACTS)	Embedded Networking	Embedded Networking	Production & Operations Management	Web Services and Service Oriented Architecture	Cloud Computing
11.08.2023 FRIDAY	Power Quality	System On Chip Architecture	Ad-hoc Wireless and Sensor Networks	Concurrent Engineering & Product Life Cycle Management	Software Quality	Software Quality
		Sensors and Actuators	Sensors and Actuators		Assurance & Testing	Assurance and Testing
16.08.2023 WEDNESDAY	Reliability Engineering	Device Modelling	Low Power VLSI	Design and Manufacturing of MEMS & Micro Systems	Number theory and Cryptography	Software Architecture and Design Patterns
		Wireless LANs and PANs				

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TIME-TABLE

II YEAR M.TECH. I SEMESTER (RA18 REGULATION - 2018, 2019 BATCHES) SUPPLEMENTARY EXAMINATIONS, JULY/AUGUST - 2023

TIME: 02.00 PM TO 05.00 PM

Date & Day	SUBJECTS (RA18 – 2018, 2019 Admitted Batches)							
	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	COMPUTER SCIENCE & ENGINEERING	ADVANCED MANUFACTURING SYSTEMS			
02.08.2023 WEDNESDAY	FLEXIBLE AC TRANSMISSION SYSTEMS (FACTS)	DESIGN FOR TESTABILITY	DESIGN FOR TESTABILITY	NATURAL LANGUAGE PROCESSING	MANUFACTURING POLYMERS			
04.08.2023 FRIDAY	OPERATIONS RESEARCH	OPERATIONS RESEARCH	OPERATIONS RESEARCH	CVDED LANG	INDUSTRIAL CASSTV			
	INDUSTRIAL SAFETY	INDUSTRIAL SAFETY	INDUSTRIAL SAFETY	CYBER LAWS	INDUSTRIAL SAFETY			

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